

Mahatma Education Society's
Pillai College of Engineering, New Panvel
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

TIME TABLE (IA-1)

CLASS: BTECH EXTC SEM - VIII (A&B)

Day	Date	Time	Subject	Question Paper Marks	Faculty
Sunday	23rd Feb 2025	10.00 AM TO 11.30 PM	Digital Business Management and Digital Marketing	40	Mrs. Rashmi Gourkar
			Technologies for Rural Development	40	Dr. Deepali Shrivastav
			Environmental Management	40	Mrs. Sonali Nikam
			Economics	40	Mr. Prashant Nair
		2.00 PM TO 3.30 PM	DLOC: Cloud Computing	40	Mrs. Harsha Ghadge

Prof. Aboli Khedkar
(Test Coordinator)

Dr. Karpagavalli S.
(Test Coordinator)

Dr. Avinash Vaidya
(HOD, EXTC)

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TIME TABLE (IA-1)

CLASS: S.E. EXTC SEM - IV (A&B)

Day	Date	Time	Subject	Question Paper Marks
Thursday	20th Feb 2025	12.00 PM to 1.30 PM	Engineering Mathematics - IV	40
		4.00 PM to 5.30 PM	Electronic Communication systems	40
Friday	21st Feb 2025	12.00 PM to 1.30 PM	Signals and Systems	40
		4.00 PM to 5.30 PM	Linear Integrated Circuits	40
Monday	24th Feb 2025	12.00 PM to 1.30 PM	Microprocessor and Microcontroller	40
		4.00 PM to 5.00 PM	Entrepreneurship	20

TIME TABLE (IA-1)

CLASS: T.E. EXTC SEM - VI (A&B)

Day	Date	Time	Subject	Question Paper Marks
Thursday	20th Feb 2025	12.00 PM to 1.30 PM	Wireless and Mobile Communication	40
		4.00 PM to 5.30 PM	EWRS	40
Friday	21st Feb 2025	12.00 PM to 1.00 PM	Data Structure	20
		4.00 PM to 5.30 PM	Robotics and Automation	40
Monday	24th Feb 2025	12.00 PM to 1.30 PM	Advance Network Technology	40
			Big Data Analysis	40
		2.00 PM to 3.30 PM	ILOC: Entrepreneurship	40
			ILOC: E- Commerce and E-Business	40

Aboli Khedkar
15-2-25
Prof. Aboli Khedkar

(Test Coordinator)

S. Karpagavalli
Dr. Karpagavalli S.

(Test Coordinator)

Avinash Vaidya
15-2-25
Dr. Avinash Vaidya

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